

FIG. 1 WIRELESS ACCESS REFERENCE MODEL

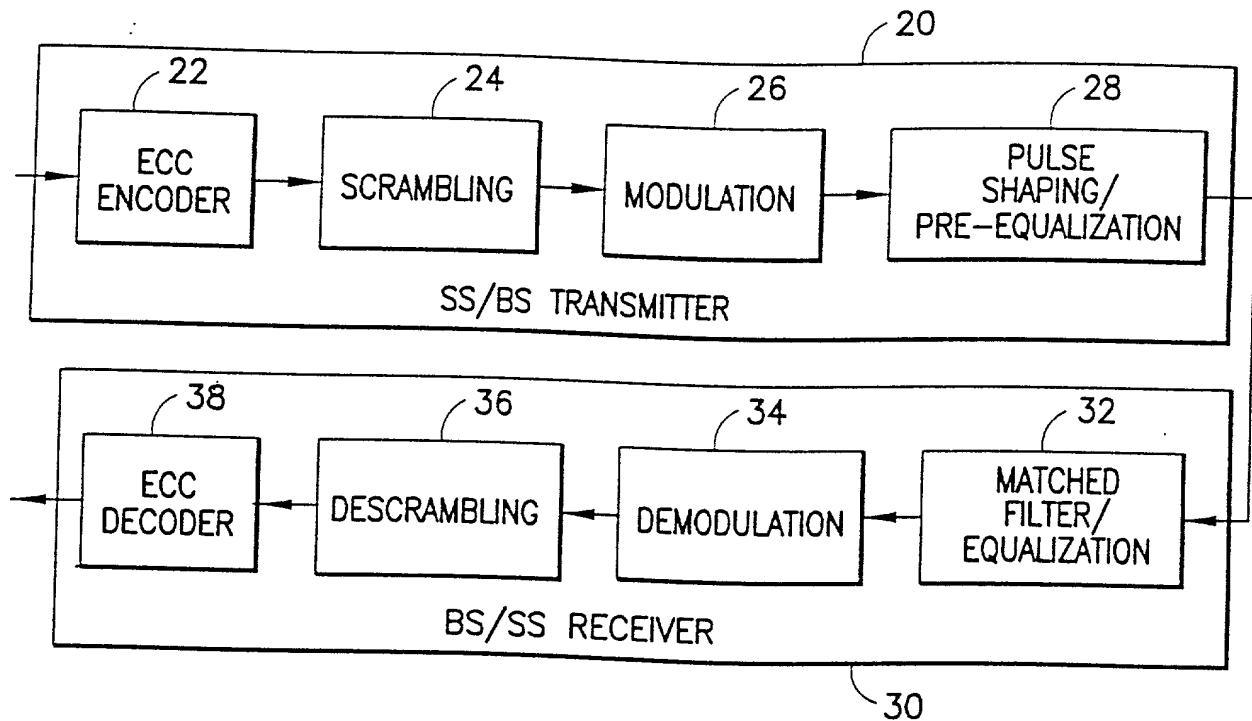


FIG. 2 PHY REFERENCE MODEL SHOWING DATA FLOW

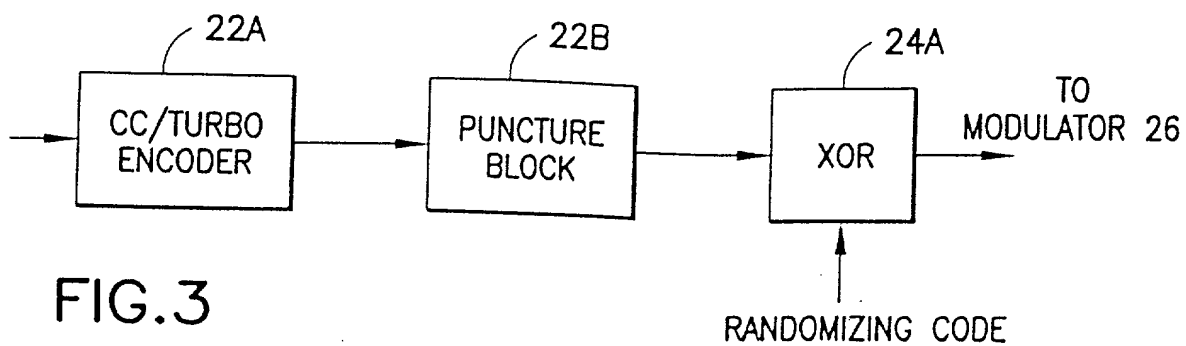


FIG. 3

PARAMETER	MODULATION AND CHANNEL CODING		
	QPSK $w/R=4/5$ CODING (1.6 BITS/SYM)	16-QAM $w/R=4/5$ CODING (3.2 BITS/SYM)	64-QAM $w/R=4/5$ CODING (4.8 BITS/SYM)
RF CHANNEL BANDWIDTH	3.5 MHz	3.5 MHz	3.5 MHz
CHIP RATE	2.56 Mcps	2.56 Mcps	2.56 Mcps
COMMUNICATION CHANNEL BANDWIDTH	4.096 Mbps	8.192 Mbps	12.288 Mbps
PEAK DATA RATE	4.096 Mbps	8.192 Mbps	12.288 Mbps
CDMA CHANNEL BANDWIDTH (SF=1)	4.096 Mbps	8.192 Mbps	12.288 Mbps
CDMA CHANNEL BANDWIDTH (SF=16)	256 kbps	512 kbps	768 kbps
CDMA CHANNEL BANDWIDTH (SF=128)	32 kbps	64 kbps	96 kbps
MODULATION FACTOR	1.17 bps/Hz	2.34 bps/Hz	3.511 bps/Hz

**FIG.4** HYPOTHETICAL PARAMETERS FOR A 3.5 MHz RF CHANNELIZATION

NUMBER OF ELEMENTS	QPSK		16 QAM		64 QAM	
	AGGREGATE CAPACITY (Mbps)	MODULATION FACTOR	AGGREGATE CAPACITY (Mbps)	MODULATION FACTOR	AGGREGATE CAPACITY (Mbps)	MODULATION FACTOR
1	4.096	1.17	8.192	2.34	12.288	3.511
2	8.192	2.34	16.384	4.68	24.576	7.022
4	16.384	4.68	32.768	9.36	49.152	14.044
8	32.768	9.36	65.536	18.72	98.304	28.088
16	65.536	18.72	131.072	37.44	196.608	56.176

### FIG.5 AGGREGATE CAPACITY AND MODULATION FACTORS VERSUS MODULATION TYPE AND ARRAY SIZE

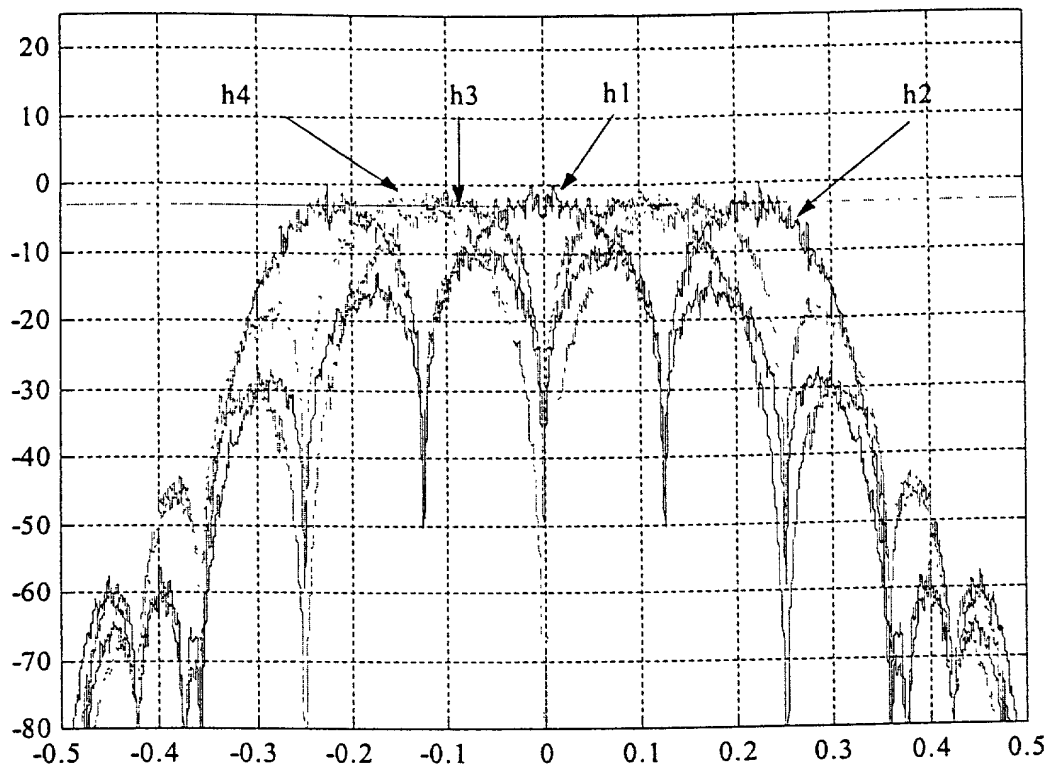


FIG. 7A

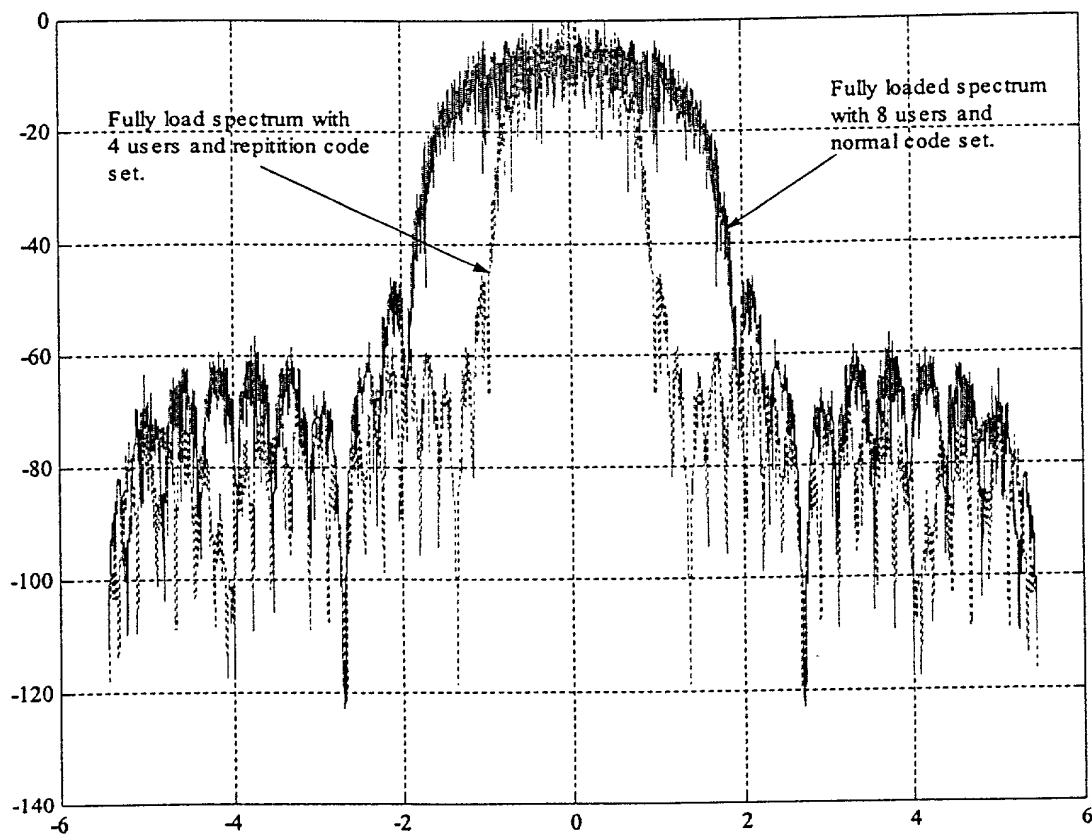


FIG. 7B

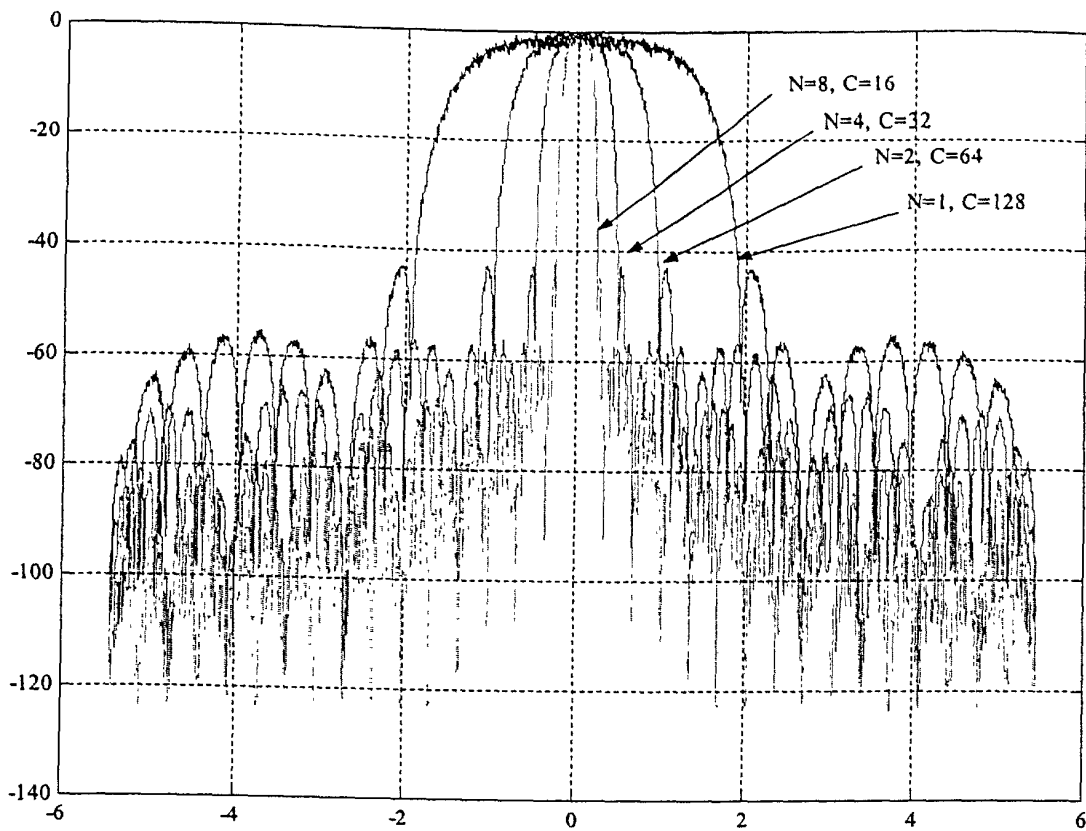


FIG. 7C

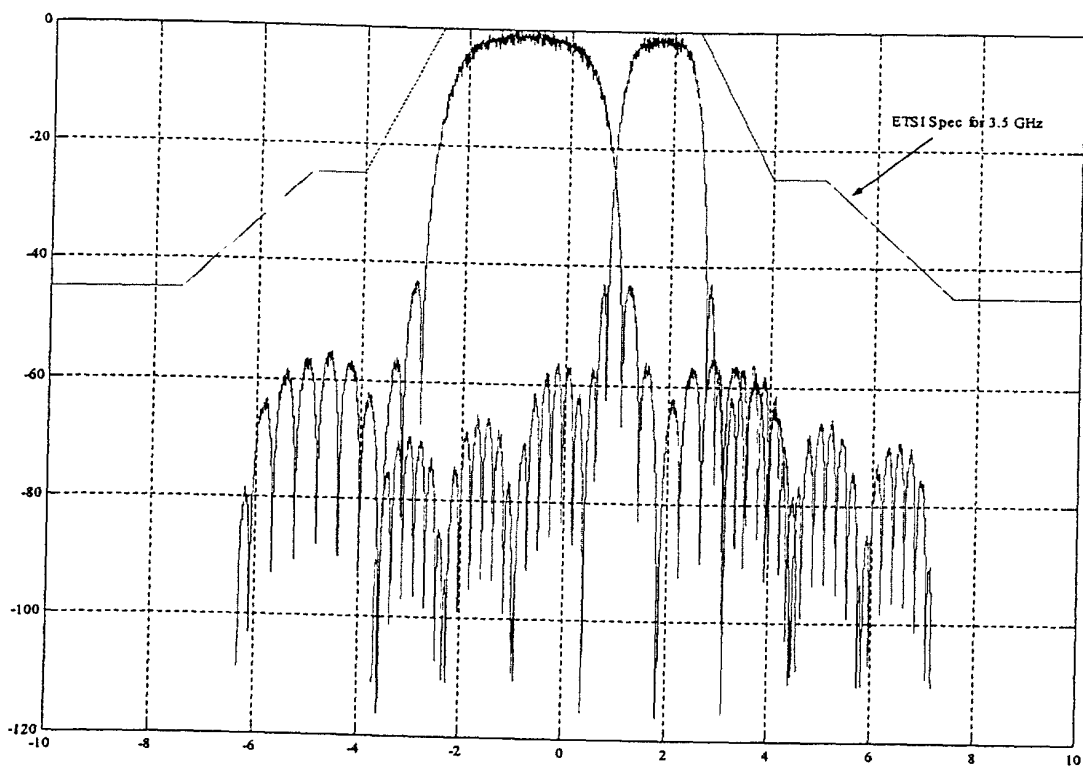


FIG. 7D

$$H_4 = \begin{bmatrix} +1 & +1 & +1 & +1 \\ +1 & -1 & +1 & -1 \\ +1 & +1 & -1 & -1 \\ +1 & -1 & -1 & +1 \end{bmatrix} = \begin{bmatrix} h_1 \\ h_2 \\ h_3 \\ h_4 \end{bmatrix}$$

FIG. 6A

$$H_{4 \times 8} = \begin{bmatrix} +1 & +1 & +1 & +1 & +1 & +1 & +1 & +1 \\ +1 & +1 & -1 & -1 & +1 & +1 & -1 & -1 \\ +1 & +1 & +1 & +1 & -1 & -1 & -1 & -1 \\ +1 & +1 & -1 & -1 & -1 & -1 & +1 & +1 \end{bmatrix}$$

FIG. 6B

$$H_{8 \times 8} = \begin{bmatrix} +1 & +1 & +1 & +1 & +1 & +1 & +1 & +1 \\ +1 & -1 & +1 & -1 & +1 & -1 & +1 & -1 \\ +1 & +1 & -1 & -1 & +1 & +1 & -1 & -1 \\ +1 & -1 & -1 & +1 & +1 & +1 & -1 & +1 \\ +1 & +1 & +1 & +1 & -1 & -1 & -1 & -1 \\ +1 & -1 & +1 & -1 & -1 & +1 & -1 & +1 \\ +1 & +1 & -1 & -1 & -1 & -1 & +1 & +1 \\ +1 & -1 & -1 & +1 & -1 & +1 & +1 & -1 \end{bmatrix}$$

FIG. 6C

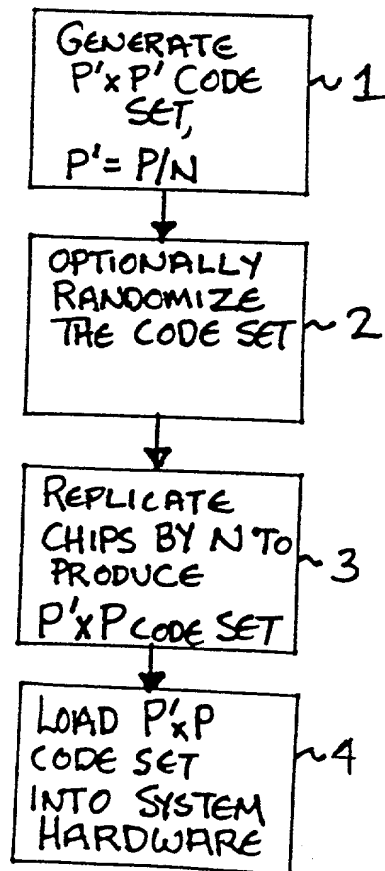


FIG. 8